

KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

(As Autonomous Institute uncler Kakatiya University, Warangal)

Opp: Yerragattugutta, Bheemaram (V), Hasanparthy (Mandal), WARANGAL - 506 015, Telangana State, INDIA. काकतीय प्रौद्योगिकी एवं विज्ञान संस्थान, बरंगल - ५०६ ०१५ तेलंगाना राज्य, भारत

కాకతీయ సొంరెకేతిక విబ్బాన -రాస్త్ర విద్యాలయం, వరంగల్ - 506 015. కెలంగాణ రాష్ట్రం, భారశదేశము Office of the Controller of Examinations

for Supplementary Students

2018 to 2021 Admitted Batches

No. URR18 - B.Tech. III-Sem./ESE Schedule Supply /2024

Date: 11-11-2024

## END SEMESTER EXAMINATIONS (ESE) OF URR18 B.TECH. III-SEMESTER SUPPLEMENTARY EXAMINATIONS, NOVEMBER/DECEMBER-2024

TIME - TABLE

Examination Timings: FN: 09.30 am to 12.30 pm

Date & Day of Examination	CE	ME	CSE	EEE	ECE	IT	CSE(N)	ECI	CSE (IoT)	CSE (AIML)
25.11.2024 Monday	Engineering Mathematics-III	Engineering Mathematics - III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-II
27.11.2024 Wednesday	<b>OE-I</b> Strength of Materials	Mechanics of Solids	Object Oriented Programming through JAVA	Electrical Measurements & Instrumentation	OE-I Object Oriented Programming	Operating Systems	Object Oriented Programming through JAVA	OE-I Object Oriented Programming	Object Oriented Programming through JAVA	Object Oriented Programming through JAVA
29.11.2024 Friday	Fluid Mechanics	Material Science and Metallurgy	Discrete Mathematics	Electromagnetic Fields	Signals & Systems	Discrete Mathematics	Database Management Systems Operating Systems	Electronic Measurements and Sensors	Fundamentals of Internet of Things	Operating Systems
02.12.2024 Monday	Surveying	Engineering Thermodynamics	Computer Architecture and Organization	Network Theory	Network Analysis	Computer Architecture and Organization	Computer Architecture and Organization	Signals Systems and Random Processes	Computer Networks	Computer Organization and Architecture
04.12.2024 Wednesday	Construction Materials	Machine Drawing	Advanced Data Structures	Electronic Devices and Circuits	Analog Circuits - I	Advanced Data Structures Object Oriented Programming through C++	Advanced Data Structures	Electronic Devices and Applications	Advanced Data Structures	Advanced Data Structures
06.12.2024 Friday		-	Digital Electronics	_	Switching Theory & Logic Design	Switching Theory and Logic Design	Digital Electronics	Digital Circuits and Logic Design	Digital Electronics	Formal Languages and Automata Theory
09.12.2024 Monday	-	Essence of Indian Traditional Knowledge	Essence of Indian Traditional Knowledge	-		Essence of Indian Traditional Knowledge	Essence of Indian Traditional Knowledge	_	Essence of Indian Traditional Knowledge	<u>.</u>

Candidate should occupy his/her seat at least 5 minutes before the commencement of the examination.

To: Exam Branch Notice Boards

Copy to: 1. Principal for favour of information 2. Dean, Academic Affairs 3. HOD's - with a request to display in the Dept Notice Boards & share in the Class WhatsApp groups

4. Training & Placement Officer 5. I/c Web Team - with a request to display on the kitsw.ac.in/examinations/Examinations\_schedules.html